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Selective Electroless Copper Deposition by Using Photolithographic Polymer/Ag Nanocomposite

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Abstract—This paper presents a novel, direct, selective, vacuum-free, and low-cost method of electroless copper deposition, allowing additive patterning of nonconductive surfaces. Ag nanoparticles (NPs) synthesized inside a photosensitive polymer are acting as seeds for electroless copper deposition. The resulting copper film surface morphology was studied with scanning electron microscopy. Copper films were shown to display a rough grainlike structure, covering substrate uniformly with good metal-substrate adhesion. Copper thickness was studied as a function of the plating time, temperature, and Ag NPs seed concentration. A maximal copper thickness of $0.44 \pm 0.05 \mu\text{m}$ was achieved when plated at 30°C with 0.4 M Ag(I) . The minimum feature resolution of copper patterns, grown with 0.025- and 0.1-M silver salt, is attained down to $10 \mu\text{m}$. The maximum electrical conductivity of the copper film prepared with 0.025- , 0.1- , and 0.4-M Ag(I) approaches $(0.8 \pm 0.1) \times 10^7 \text{ S/m}$, $(1.1 \pm 0.1) \times 10^7 \text{ S/m}$ and $(1.6 \pm 0.4) \times 10^7 \text{ S/m}$, respectively. Electroless copper interconnections and LED circuit on glass substrate were fabricated as a proof of concept demonstrators.

Index Terms—Ag nanoparticles (NPs) seeds, electroless copper, metallization of nonconductive surfaces, polymer/Ag nanocomposite.

I. INTRODUCTION

ELECTROLESS metal deposition is a facile method to fabricate metallic patterns on nonconductive substrates.

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Electroless plating of sensor electrodes and interconnects in microtechnology and nanotechnology, such as functional printing, flexible electronics, wearable electronics, and the “Internet of Things,” can reduce production cost as compared to other traditional metallization techniques [1]. Conventional track formation is a multistep photolithographic method producing conductive patterns by the removal of unwanted metal parts through etching processes. This pattern transfer method thus generates large quantities of waste. An alternative method is to deposit the metal by sputtering or evaporation. However, this process involves costly equipment and complex processing that consume high power to deposit metal layers. Moreover, vacuum requirements in processing increase the cost of the conventional metal patterning. Inkjet printing is another technique for printing conductive patterns on a substrate [2]. However, inkjet printing suffers from low manufacturing capability, while photolithography remains a dominant mass production method in the microelectronics industry [3]–[5]. Hence, there is continuous research in conductive track formation. Attempts have been made in metal patterning by using a combination of photolithography and electroless plating [6]. To fabricate metal patterns on nonconductive surfaces, a continuous seed layer is required [7]. The seed layer could be fabricated using a photolithographic approach, where metal NPs are synthesized inside a photosensitive polymer matrix, as reported by Marques-Hueso *et al.* [8], [9]. Metal NPs embedded inside the polymer can be used for further processing, such as nanostructure precursors [10], and it is viable that they could act as seeding sites for metal ions adsorption and continuous electrode formation.

Ag has successfully been used as a metal catalyst for electroless copper plating (ECP) [6]. Pd is a choice catalytic material for electroless plating due to its high activity [11]; however, palladium price per gram exceeds silver price by almost 70 times, limiting its use for large-scale production.

In this paper, we propose to fabricate copper microfeatures by ECP on the patterns formed by photolithography, using more economic Ag NPs as catalysts, as shown in Fig. 1. We demonstrate the formation of conductive copper tracks through the use of 2-diazo-2H-naphthalen-1-one (DNQ)-novolac polymer/Ag (hereafter—polymer/Ag) nanocomposite positive pattern on glass and silicon surfaces by photolithography, and its conversion to a copper pattern via electroless

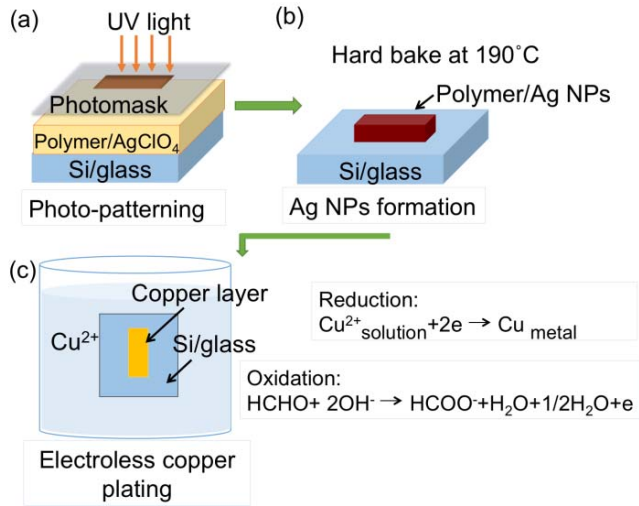


Fig. 1. Process steps for photopatterning and selective Cu deposition on silicon and glass substrates using (a) photolithography, (b) Ag NPs synthesis inside the polymer, and (c) direct deposition of Cu by electroless plating.

plating. This method benefits from not requiring vacuum, Ar and N₂ gaseous environments, and expensive equipment. The lithographic properties of the polymer allow direct photopatterning of metal tracks and interconnect on a desired substrate. The effect of the production parameters, such as plating temperature, plating time, and Ag NPs concentration, is discussed in this paper with a particular attention to copper film thickness, quality, feature resolution, and adhesion. Furthermore, adhesion of the copper film to a substrate and its effect on the copper film conductivity is also studied. Resilient interconnections between metal pads are demonstrated by the developed ECP method.

II. EXPERIMENTS

A. Materials

This paper used the following materials. Silver perchlorate (Sigma Aldrich, 97%), 1-methoxy 2-propylacetate (MPA) (Sigma Aldrich, 99%), DNQ-novolac positive tone photoreist (Ma-P 1215, Microresist Technology), AZ 326 developer (Microresist Technology), copper sulfate (98%, Sigma Aldrich), sodium hydroxide (98.5%, Acros Organics) potassium tartrate (99%, Fisher Scientific), and formaldehyde (37%, Acros Organics).

B. Synthesis of Ag NPs Seed Layer

0.025-, 0.1-, and 0.4-M silver perchlorate (AgClO₄) concentrations were prepared by direct dissolution in MPA. DNQ-novolac photosensitive polymer (hereafter—polymer) was mixed with the silver solution in 1:1 ratio. The resultant mixture was spin coated on silicon and glass substrates at 3000 r/min for 30 s and soft baked at 45 °C for 10 min. Silicon (15 mm × 15 mm) and glass (18 mm × 18 mm) substrates were exposed to UV light at 14 mW · cm⁻² in Karl Suss MJB 3 UV mask aligner and developed with AZ 326 developer diluted with deionized water (DI) in 1:1 ratio. The samples were hard baked at 190 °C for 10 min on a hot plate

leading to the thermal reduction of Ag(I) to Ag NPs inside the polymer.

C. Fabrication of Electroless Copper Films and Interconnections

Electroless copper bath solution was prepared using the formulation in [12] by dissolving 6 g of copper sulfate, 8 g of sodium hydroxide, and 28 g of potassium tartrate into 200 ml of DI water. Before electroless plating, the concentrated solution was diluted with DI water in the ratio of 1:1 and 2 ml of formaldehyde was added. Treated substrates were immersed into electroless copper bath for the allocated time. Electroless plating was conducted at room temperature (RT) and 30 °C.

Metal pads were fabricated as a testing environment to study electroless copper interconnection behavior. Test metal pads consisted of 200-nm copper on 50-nm Ti and were fabricated by UV-lithography, e-beam thermal evaporation, and liftoff process. The sizes of the fabricated test metal pads are 300 μm × 300 μm and 500 μm × 500 μm.

D. Characterizations

The presence of Ag NPs and their optical and structural properties were characterized by a UV-2550 spectrophotometer. Copper film thickness was measured by Dektak³ Stylus optical profilometer. To obtain the copper thickness for each configuration, three samples were prepared and average copper thicknesses with corresponding standard deviations were calculated. Optical images of the film surface were obtained using a LEICA CTR 6500 microscope. The surface morphology of copper film was studied with scanning electron microscopy (SEM) Quanta 650 field emission gun.

The conductivity of the copper films was obtained by four-point probe using the van der Pauw technique [13]. To acquire copper conductivity for each configuration, three samples were prepared and average copper conductivity with corresponding standard deviations was calculated for each of the samples. Square glass substrates (22 mm × 22 mm) were used for copper layer deposition. The van der Pauw method involves the application of a current (I), and the measurement of the resulting voltage (V) using four small contacts on the perimeter of the sample. Therefore, four probes were attached at the edges of the square sample. Four measurement configurations at each probe location for forward and reverse current were performed, and conductivity was found as an inverse of the resistivity, where resistivity, ρ , was calculated as [14], [15]

$$\rho = \frac{\pi}{\ln(2)} t \frac{(R_a + R_b)}{2} F \quad (1)$$

where t is a copper thickness, F is a correction factor, and R_a and R_b are as follows:

$$R_a = \frac{\frac{V_{f34}}{I_{f21}} + \frac{V_{r34}}{I_{r21}} + \frac{V_{f12}}{I_{f43}} + \frac{V_{r12}}{I_{r43}}}{4} \quad (2)$$

$$R_b = \frac{\frac{V_{f41}}{I_{f32}} + \frac{V_{r41}}{I_{r32}} + \frac{V_{f23}}{I_{f14}} + \frac{V_{r23}}{I_{r14}}}{4} \quad (3)$$

The subscripts f and r stand for forward and reverse current directions, respectively. For symmetrical samples, such as the square, $F = 1$ [14].

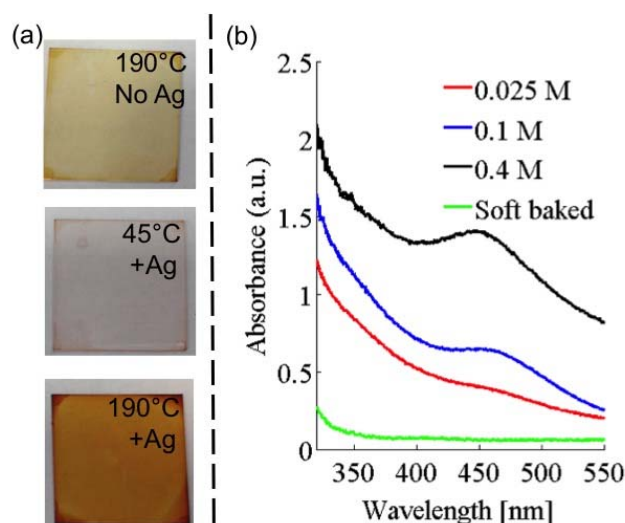


Fig. 2. (a) Images of glass substrates (see descriptions on the figure) and (b) UV-vis absorbance spectra of polymer/Ag nanocomposite prepared with 0.025-, 0.1-, and 0.4-M Ag(I) salt after samples were hard baked at 190 °C, and a reference sample acquired after a soft bake at 45 °C.

Resistance of the tests pads with electroless copper interconnections was measured using two probes in the SIGNATONE S-1160 probe station. The diameter of the probe tips is 80 μm with the electrodes spaced at around 1 mm. Test metal pads of 500 μm \times 500 μm sizes were used for resistance characterizations. Parasitic resistances were assumed to be negligible.

III. RESULTS AND DISCUSSION

A. Ag NPs Seed Layer Characterization

Fig. 2(a) shows the appearance of polymer and polymer/Ag structures on glass substrates after a hard (190 °C) and soft (45 °C) bake. Polymer/Ag structure changes color from light yellow to dark brown when hard baked, indicating a formation of Ag NPs by thermal reduction [9]. Fig. 2(b) shows UV-vis absorbance spectra of the polymer/Ag nanocomposites after a hard and soft bake. Ag NPs display localized surface plasmon resonance (LSPR) occurring near 400–500 nm [16]. Therefore, LSPR maxima occurring here indicates the presence and formation of Ag NPs during a hard bake for the three Ag(I) salt concentrations. Single and broad LSPR peak indicates that Ag NPs have spherical shapes with a broad size distribution and sizes up to 100 nm [17]. LSPR intensity rises with higher Ag(I) salt concentrations, which implies that the amount of Ag NPs embedded inside the polymer increases [18].

B. Effect of Plating Time, Temperature, and Ag NPs Concentration on the Copper Film

Fig. 3 displays a plot of the plated copper thicknesses on the silicon substrate as a function of plating time. Copper thickness grows linearly with plating time which is in agreement with the previously published work [19], [20]. A maximal copper thickness of $0.44 \pm 0.05 \mu\text{m}$ was achieved when plated at 30 °C with 0.4-M Ag(I) and an increase to Ag NPs concentration also produced thicker copper layers. This may be

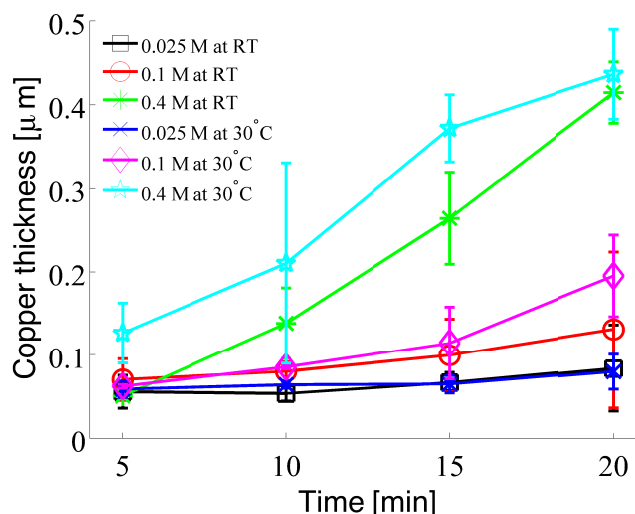


Fig. 3. Copper thickness as a function of AgClO_4 salt concentration, electroless plating time, and temperature.

due to densely packed Ag NPs in the polymer, which created more sites for copper ion adsorption at the higher temperatures and concentrations.

The effect of the plating temperature on the copper surface morphology is shown in Fig. 4(a)–(c). SEM micrographs taken after 10 min of ECP show that the copper film has a rough granular structure covering the polymer surface uniformly, when plated both at RT and at 30 °C. However, the surface of the copper film deposited at RT has voids between the copper grains and appears more porous compared to the copper film plated at 30 °C, as observed in [21]. Therefore, plating at 30 °C produces thicker copper layers and a more uniform and continuous copper film.

C. Effect of Ag NPs Concentration on Film Ductility

Fabricated copper films are free of cracks and defects; however, electroless copper films frequently suffer from poor ductility due to buildup of internal stress, which was also observed in [22] and [23]. In the present technique, delamination of copper film may occur after plating time reaches a certain threshold. Fig. 4(d) and (e) shows the visual appearance of the copper film prepared with varied Ag(I) salts concentrations. Copper film grown with 0.025-M Ag(I) displayed delamination at 8 min. SEM images in Fig. 4 show qualitatively that the copper film ductility improved with the increase in Ag(I) salt concentration. For example, copper obtained with 0.1- and 0.4-M Ag(I) only delaminated after 12 min. This copper peeling process can be understood by analyzing the copper growth mechanism. The growth of a copper thin film (up to 1 μm) undergoes three stages: nucleation, growth, and coalescence of three-dimensional crystallites (TDCs) [24]. Coalescence is the most important stage, in which crystal building process results in the formation of a continuous copper film. Therefore, copper cracks may occur due to the built-up stress and formation of voids between TDC. Also, since the reduction of both copper and hydrogen takes place simultaneously, the probability of incorporating H and H_2 into the copper deposition is high [25]. Hydrogen traps contribute

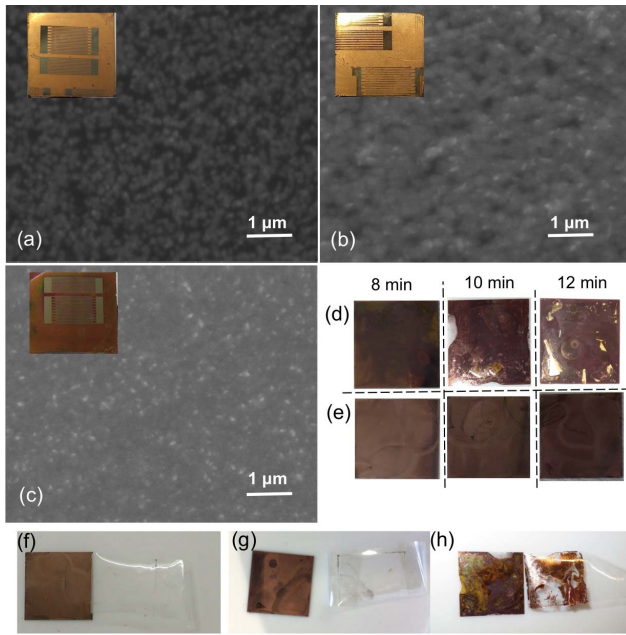


Fig. 4. SEM micrographs of the copper surface prepared with (a) 0.4-M Ag(I) and plated at RT for 10 min, (b) 0.4-M Ag(I) and plated at 30 °C for 10 min, and (c) 0.1-M Ag(I) and plated at 30 °C for 10 min. Insets: photographs of micropatterns on silicon substrates. Photographs of the copper films on the glass substrates plated at 30 °C for 12 min with Ag(I) concentrations of (d) 0.025 M and (e) 0.4 M. Photographs of the copper films on the glass substrates and corresponding tapes after a scotch-tape tests (f) 10-min ECP, 0.4-M Ag(I), (g) 10-min ECP, 0.1-M Ag(I), and (h) 10-min ECP, 0.025-M Ag(I).

to the formation of voids and may lead to a porous copper structure [26]. Therefore, Ag NP concentration should remain high to increase copper density and to eliminate void formation between the copper grains. Hence, seed layers with 0.1 and 0.4-M Ag(I) are more favorable to produce continuous copper films.

When applying the IPC-TM-650 scotch-tape tests [27] to copper films prepared at 0.4-M Ag(I) shown in Fig. 4(f), the adhesions of the copper films to glass substrates show a good strength, with no observable removal of the metal parts from the substrate. As for copper films prepared with 0.1-M Ag(I), minor removal of the copper was observed at the edges of the glass substrate [see Fig. 4(g)]. However, copper films prepared with a low-Ag NPs seed concentration [0.025-M Ag(I)] and plated beyond the plating time threshold, as shown in Fig. 4(h), display delamination due to scotch tape test.

D. Resolution and Selectivity

Fig. 5 shows optical images of copper patterns after photolithography and ECP with pitch sizes of 40 and 20 μm on silicon substrate. It is seen that copper plates uniformly on the surface of the polymer/Ag nanocomposite preserving the lithographic pattern. Copper films grown with 0.025- and 0.1-M silver salt present well-resolved features down to 10 μm . It was found that the lithographic performance of polymer is affected by the presence of Ag(I) salt, as the resolution is poorer for patterns prepared with 0.4-M silver salt. This could be due to the increased con-

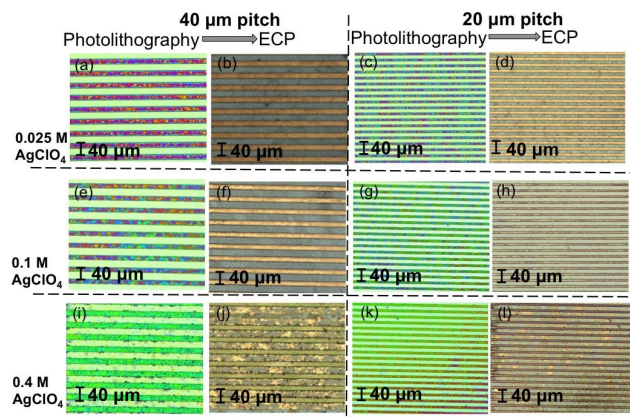


Fig. 5. Optical images of the patterns taken after photolithography and ECP, with Ag(I) concentrations of (a)–(d) 0.025 M, (e)–(h) 0.1 M, and (i)–(l) 0.4 M.

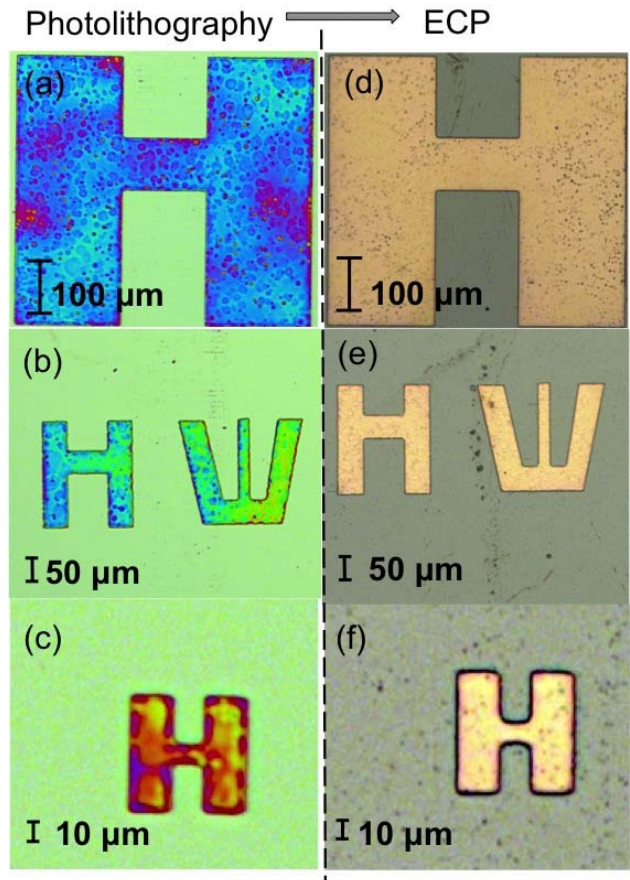


Fig. 6. Optical images of the patterns prepared with 0.025-M Ag(I). Images were taken after (a)–(c) photolithography and after (d)–(f) ECP steps.

centration of the embedded Ag NPs, leading to the poorer stripping performance of the polymer during the development step. Therefore, copper plating on the poorly developed sites affects the overall feature resolution. Fig. 6 shows optical images of the copper features with a fine resolution down to 10 μm . ECP shows a good selectivity allowing targeting and plating on specific localized areas. Therefore, this method demonstrates excellent selectivity where the copper plates uniformly only on the catalyzed surfaces.

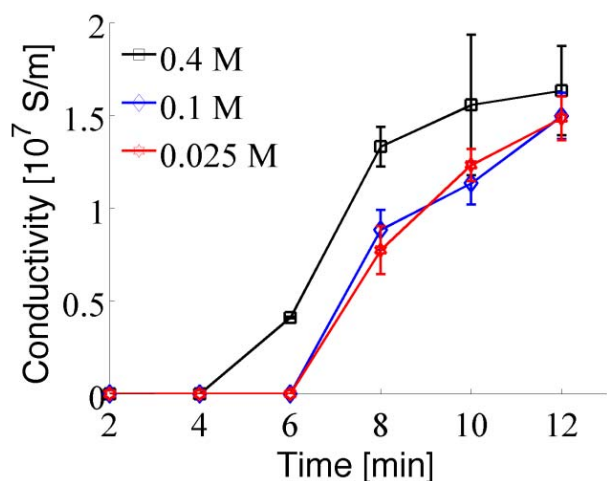


Fig. 7. Conductivity measurements at different plating times for 0.025-, 0.1-, and 0.4-M Ag(I) measured with four-point probe.

E. Influence of the Copper Thickness on Conductivity

Fig. 7 shows the electroless copper conductivity as a function of plating time for several salt concentrations. Before any copper delamination occurs, the maximum electrical conductivity of the copper film prepared with 0.025-M Ag(I) approaches $(0.8 \pm 0.1) \times 10^7$ S/m at 8 min. For copper films prepared with 0.1- and 0.4-M Ag(I), the conductivity grows to $(1.1 \pm 0.1) \times 10^7$ S/m and $(1.6 \pm 0.4) \times 10^7$ S/m at 10 min, respectively. This is close to previously reported measurements on electroless copper conductivity [23], [28]. These values compare favorably to the electrical conductivity of bulk copper at 5.96×10^7 S/m.

There is, therefore, a tradeoff between the copper thickness and copper film conductivity. Copper films prepared with 0.1- and 0.4-M Ag(I) salts are promising for interconnection fabrication since they can provide thicker copper patterns with a good adherence to a substrate while still approaching the bulk copper conductivity.

F. Electroless Copper Interconnections

As a demonstration of the electroless interconnections between the rigid parts, Fig. 8(a) and (b) shows the optical images of the interconnections after photolithography and ECP. Electroless copper successfully interconnected test pads and plated on the top of the metal pads and polymer/Ag nanocomposite. The 0.1-M Ag(I) was used and the metal features were plated for 10 min. To demonstrate that electroless interconnects are able to link pads, the electrical resistance was measured. The average resistance was found as $14 \pm 8 \Omega$. The resistance of the interconnections could be affected by the nonuniformity of the copper thicknesses of the formed interconnections ($0.59 \pm 0.06 \mu\text{m}$) and pads ($0.87 \pm 0.07 \mu\text{m}$). The growth of the electroless copper depends on the catalytic activity of the seeds [29]. Evaporated copper consists of closely packed nanocrystallites, which are distributed uniformly on the substrate [30], [31]. Therefore, copper nanocrystallites act as seeds for electroless copper

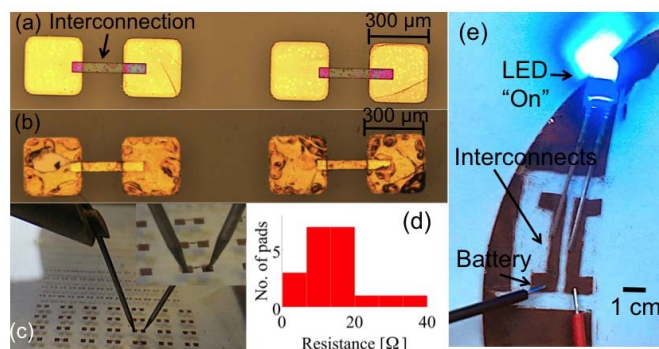


Fig. 8. (a) Images of the $300 \mu\text{m} \times 300 \mu\text{m}$ pads with the electroless copper interconnections after photolithography and (b) after ECP. (c) Measurement of the resistance using two probes with the probes placed on $500 \mu\text{m} \times 500 \mu\text{m}$ pads. Distance between the pads is $500 \mu\text{m}$. Inset: a magnified image of the probe tips landing on the pads. (d) Distribution of the measured resistances per number of pads. (e) LED circuit connected to a 3-V battery.

growth. A high coverage of the surface and a reduction in seed size provides a larger surface area enabling faster plating rates as there is less incorporation of hydrogen bubbles influencing copper ductility [29]. Therefore, the plating rates for electroless copper onto evaporated copper are faster than on the polymer/Ag nanocomposites, as Ag seeds in the polymer are rather widely distributed in the polymer matrix which makes the plating rate slower [29]. Therefore, electroless copper plated onto evaporated copper metal results in a thicker copper layer than plated onto polymer/Ag nanocomposite.

An LED circuit was constructed to further demonstrate the electroless copper metallization quality and electrical conductivity. Fig. 8(e) illustrates the functional LED circuit on a glass substrate prepared using a homemade photomask. The electroless copper interconnections were prepared with 0.1-M Ag(I) and plated for 10 min. The 3-mm LED was placed on the fabricated copper tracks and a 3-V battery was connected. When the battery was switched on, the LED turned on, which signifies that the prepared electroless copper has a good electrical conductivity.

IV. CONCLUSION

Uniform copper films with good electrical properties were obtained on silicon and glass substrates by a newly developed ECP method that minimizes processing steps. Ag NPs embedded inside the polymer successfully induced the activation of noncatalytic surfaces. It was found that the electroless copper thickness depends on the concentrations of the Ag NPs, plating temperature, and plating time. Copper films prepared with 0.1- and 0.4-M Ag(I) salts have stronger adhesion to the substrates than copper films prepared with 0.025-M Ag(I). The minimum fabricated feature size is $10 \mu\text{m}$, which is the best resolution obtained with 0.025- and 0.1-M Ag(I). The conductivity of the fabricated electroless copper films is in the range of bulk copper conductivity. The method has been demonstrated to be suited for cost-effective metallization for conductive track formation.

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